



JAN-26-2006 16:43

KENYON KENYON

14089757501

P.02/03

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Pilar Rodriguez (Depositor's name)
Pilar Rodriguez (Signature)
January 26, 2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/751,750	12/29/2000	Talal K. Jaber	2207/10083	5033

TITLE OF INVENTION: MICROPROCESSOR ON-CHIP TESTING ARCHITECTURE AND IMPLEMENTATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
Nonprovisional	NO	\$1400	\$300	\$1700	01/31/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS			
LAMARRE, GUY J	2133	714-733000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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INTEL CORPORATION

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Sumit BhattacharyaDate January 26, 2006Registration No. 51,469

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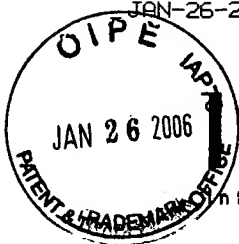
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Application No. : 09/751,750 Confirmation No. 5033
Applicant : Talal K. JABER
Filed : December 29, 2000
Title : MICROPROCESSOR ON-CHIP TESTING ARCHITECTURE AND IMPLEMENTATION
TC/A.U. : 2133
Examiner : Guy J. LAMARRE

PAPER(s) ENTITLED: PART B - FEE(S) TRANSMITTAL (plus 1 copy) 2 pages

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